Appln No.: New Divisional Application Page Applicant(s): Masayuki SAKAKURA et al. SEMICONDUCTOR DEVICE, DISPLAY DEVICE, AND METHOD OF MANUFACTURING THE SAME

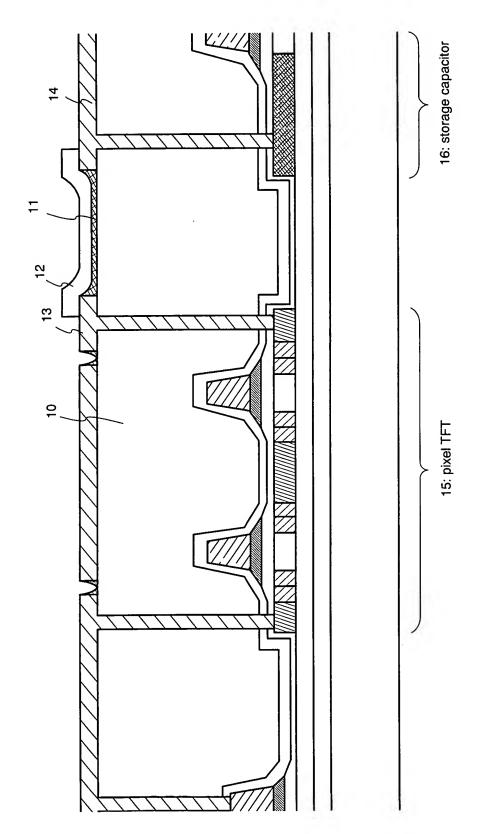


FIG.

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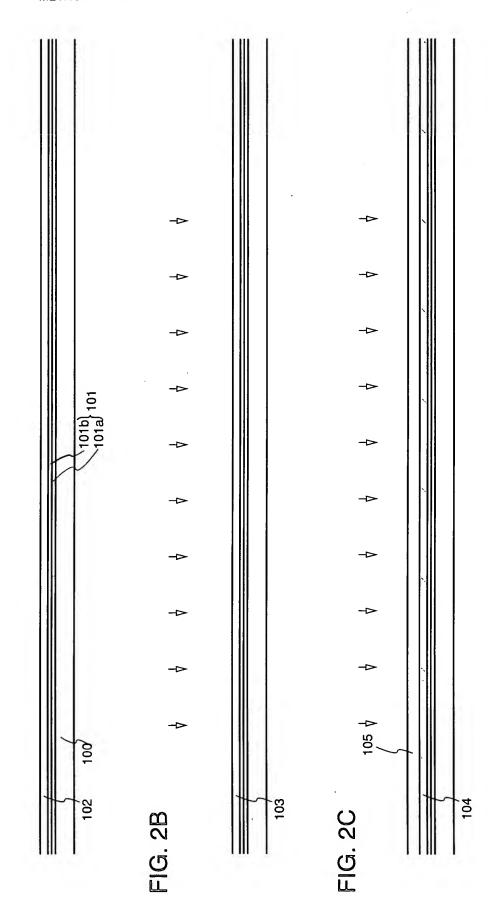
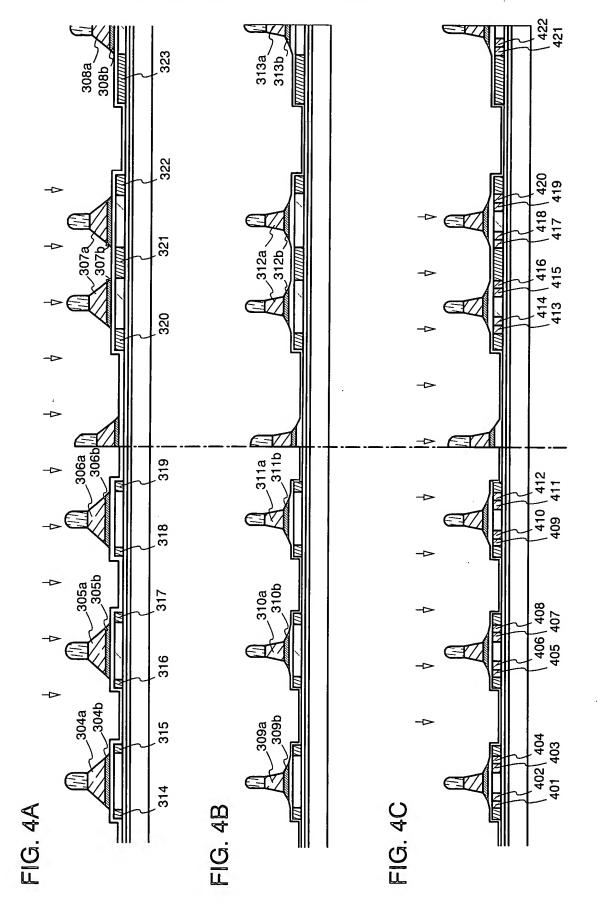
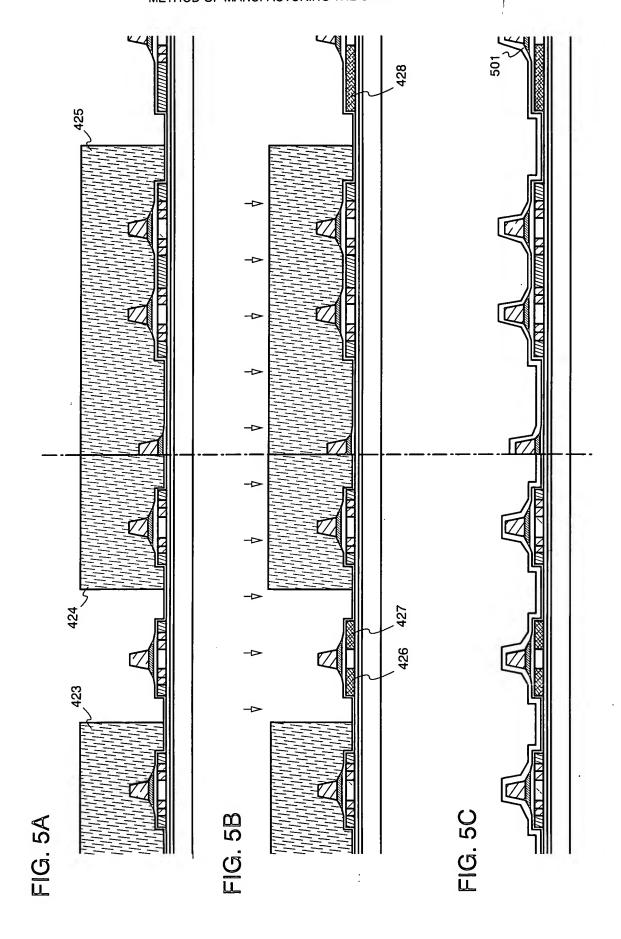


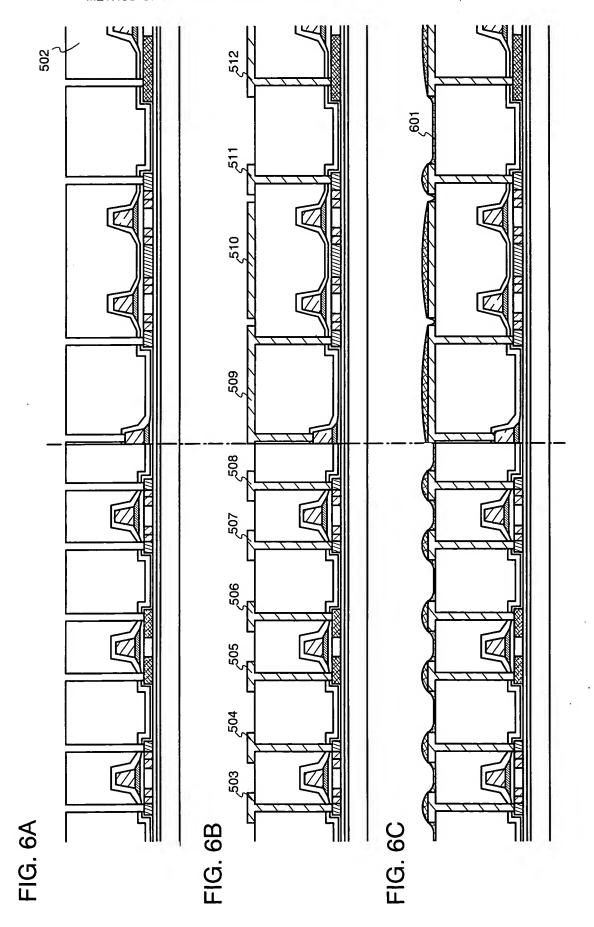
FIG. 3A

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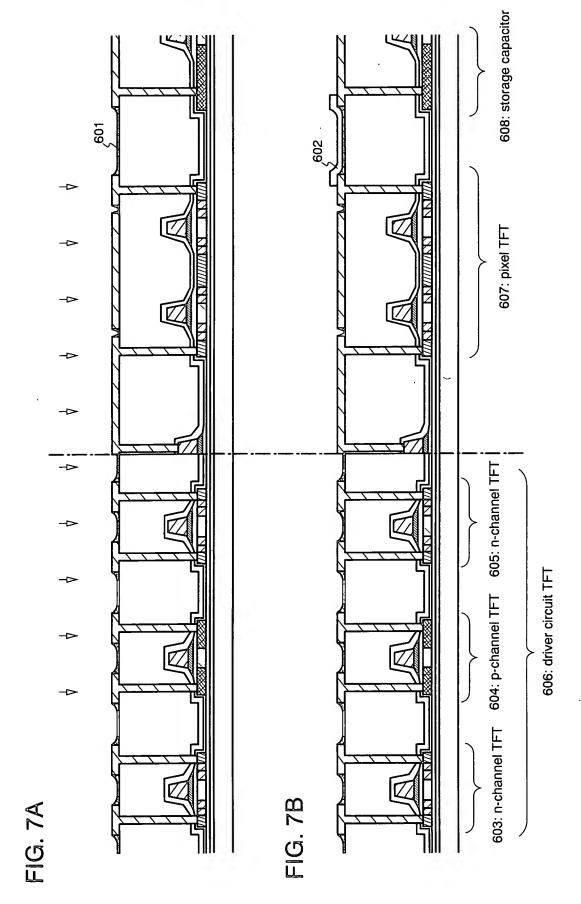




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806: storage capacitor 803 804 805: pixel TFT FIG. 8 prior art

FIG. 9

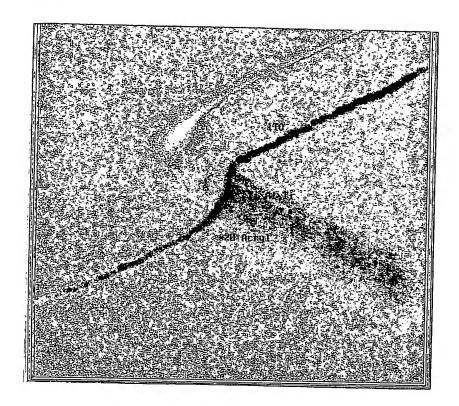
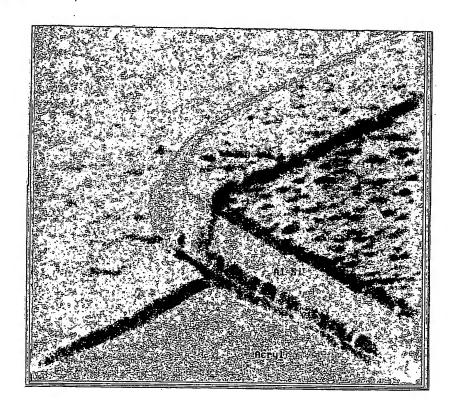
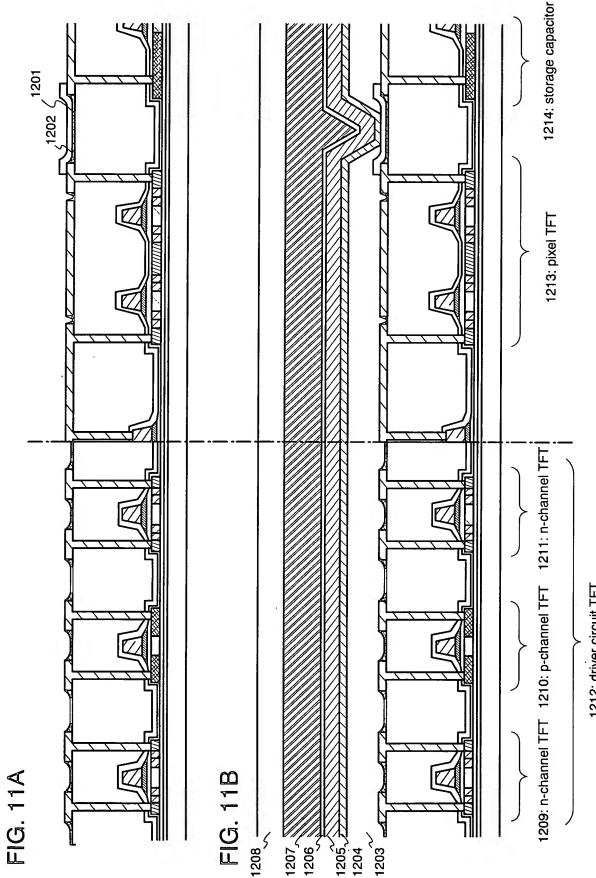


FIG. 10 prior art



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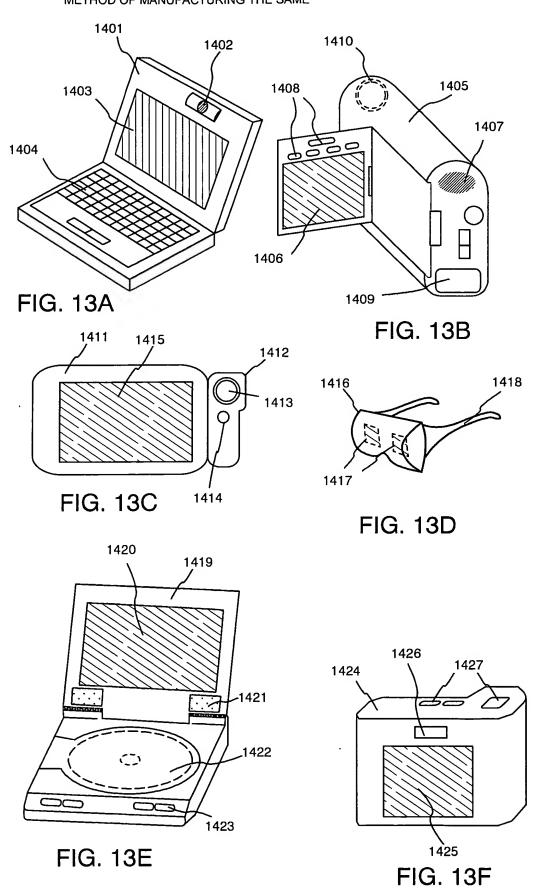
1212: driver circuit TFT

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FIG 12

FIG. 12		_
	1313: source signal line driver circuit	
,	1302: shift register circuit	
	1303: level shifter circuit	<u>.</u> ;
	1304: buffer circuit	
	1305: sampling circuit	
	1310: pre-charge circuit	
1312: gate signal line driver circuit 1306: shift register circuit 1307: level shifter circuit 1308: buffer circuit 1311: first protection circuit	1301: pixel region	1311: first protection circuit 1309: second protection circuit

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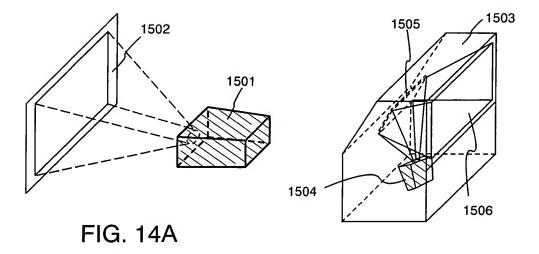


FIG. 14B

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